LPC810 by NXP

32-bit ARM Cortex M0

- 32-bit ARM Core
- upto 30MHz CPU clock
- ARMv6 Instruction Set
- Thumb + Thumb2 instructions
- 32-bit hardware multiply

Micro-controller features

· 8pin DIP Package
· 4Kb Program space
· 1Kb RAM
· 2 USARTS, I^{2}C, SPI
· Analog Comparators
· Builtin Bootloader ROM, or JTAG

Design Tools

- Commercial and Free to use IDE tools
- OpenSource GCC compiler
- FOSS tools to use bootloader rom
- Optional JTAG

Summary

- 32-bit ARM core in an 8pin package !
- Larger devices are all Surface Mount
- Only 55p each (farnell)
- Lacks A2D convertors
- Builtin Boot Loader
- GCC support